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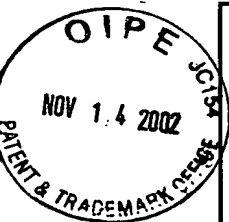


PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application Number	09/652,003
		Filing Date	August 31, 2000
		First Named Inventor	Graham Kirsch
		Group Art Unit	2186
		Examiner Name	W. Choi
Total Number of Pages in This Submission		Attorney Docket Number	M4065.0340/P340

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Other Enclosure(s) (please identify below)
<div style="text-align: right;"> RECEIVED NOV 18 2002 Technology Center 2100 </div>		
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual Name	DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP Thomas J. D'Amico
Signature	
Date	November 14, 2002

AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0340/P340

Application No.
09/652,003

Filing Date
August 31, 2000

Examiner
W. Choi

Art Unit
2186

Applicant(s): Graham Kirsch

Invention: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	48	- 51 =		x	0.00
Independent Claims	7	- 7 =		x	0.00

Multiple Dependent Claims (check if applicable) ☐

Other fee (please specify):

TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:

☒ Large Entity

☐ Small Entity

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☐ Payment by credit card. Form PTO-2038 is attached.

☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073 as described below. A duplicate copy of this sheet is enclosed.

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☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

Thomas J. D'Amico
Attorney Reg. No.: 28,371

Dated: November 14, 2002

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b/a.
SR
11-18-02
Docket No.: M4065.0340/P340
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Graham Kirsch

Application No.: 09/652,003

Group Art Unit: 2186

Filed: August 31, 2000

Examiner: W. Choi

For: METHOD AND APPARATUS FOR
CONNECTING A MASSIVELY PARALLEL
PROCESSOR ARRAY TO A MEMORY
ARRAY IN A BIT SERIAL MANNER

AMENDMENT

Commissioner for Patents
Washington, DC 20231

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Dear Sir:

In response to the Office Action dated August 14, 2002 (Paper No. 4), please amend the above-identified U.S. Patent application as follows:

IN THE CLAIMS

Please cancel claims 18, 42, 50.

Please rewrite claims 19, 20, 41, 43, 44, 48, and 51 as follows:

21
19. The circuit according to claim 20, wherein said output of said second tri-state device is coupled to a different data bus than said output of said first tri-state device.